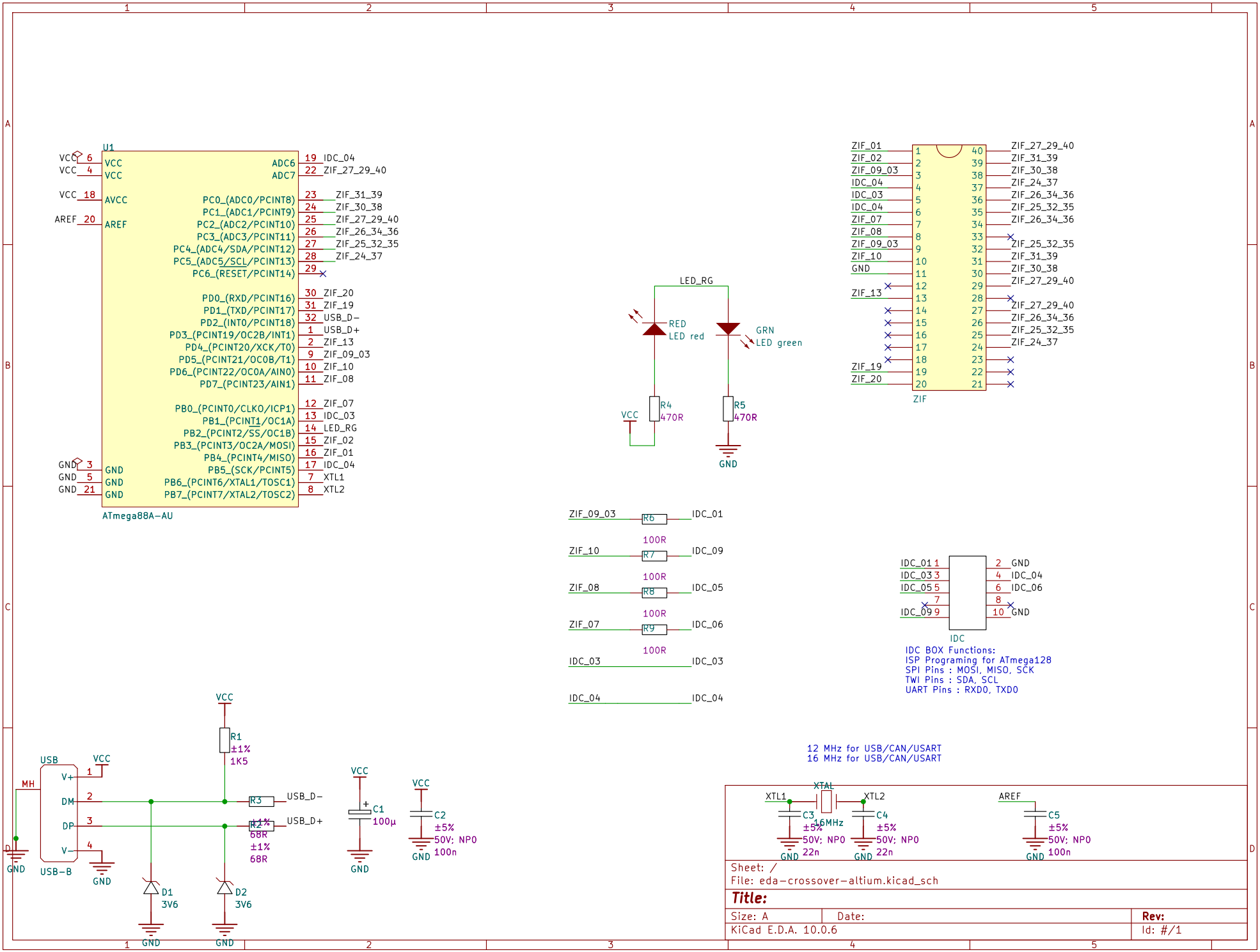

U1
VCC 6
VCC 4
VCC 18
AREF 20
GND 3
GND 5
GND 21
VCC
VCC
AVCC
AREF
GND
GND
GND
ADC6
ADC7
PC0_(ADC0/PCINT8)
PC1_(ADC1/PCINT9)
PC2_(ADC2/PCINT10)
PC3_(ADC3/PCINT11)
PC4_(ADC4/SDA/PCINT12)
PC5_(ADC5/SCL/PCINT13)
PC6_(RESET/PCINT14)
PD0_(RXD/PCINT16)
PD1_(TXD/PCINT17)
PD2_(INT0/PCINT18)
PD3_(PCINT19/OC2B/INT1)
PD4_(PCINT20/XCK/T0)
PD5_(PCINT21/OC0B/T1)
PD6_(PCINT22/OC0A/AIN0)
PD7_(PCINT23/AIN1)
PB0_(PCINT0/CLKO/ICP1)
PB1_(PCINT1/OC1A)
PB2_(PCINT2/SS/OC1B)
PB3_(PCINT3/OC2A/MOSI)
PB4_(PCINT4/MISO)
PB5_(SCK/PCINT5)
PB6_(PCINT6/XTAL1/TOSC1)
PB7_(PCINT7/XTAL2/TOSC2)
19 IDC_04
22 ZIF_27_29_40
23 ZIF_31_39
24 ZIF_30_38
25 ZIF_27_29_40
26 ZIF_26_34_36
27 ZIF_25_32_35
28 ZIF_24_37
29
30 ZIF_20
31 ZIF_19
32 USB_D−
1 USB_D+
2 ZIF_13
9 ZIF_09_03
10 ZIF_10
11 ZIF_08
12 ZIF_07
13 IDC_03
14 LED_RG
15 ZIF_02
16 ZIF_01
17 IDC_04
7 XTL1
8 XTL2
ATmega88A−AU
LED_RG
RED
LED red
GRN
LED green
VCC
R4
470R
R5
470R
GND
ZIF_01 1
ZIF_02 2
ZIF_09_03 3
IDC_04 4
IDC_03 5
IDC_04 6
ZIF_07 7
ZIF_08 8
ZIF_09_03 9
ZIF_10 10
GND 11
ZIF_13 12
13
14
15
16
17
ZIF_19 18
ZIF_20 19
20
40 ZIF_27_29_40
39 ZIF_31_39
38 ZIF_30_38
37 ZIF_24_37
36 ZIF_26_34_36
35 ZIF_25_32_35
34 ZIF_26_34_36
33
32 ZIF_25_32_35
31 ZIF_31_39
30 ZIF_30_38
29 ZIF_27_29_40
28
27 ZIF_27_29_40
26 ZIF_26_34_36
25 ZIF_25_32_35
24 ZIF_24_37
23
22
21
ZIF
ZIF_09_03
R6
100R
IDC_01
ZIF_10
R7
100R
IDC_09
ZIF_08
R8
100R
IDC_05
ZIF_07
R9
100R
IDC_06
IDC_03
IDC_03
IDC_04
IDC_04
IDC_01 1
IDC_03 3
IDC_05 5
7
IDC_09 9
2 GND
4 IDC_04
6 IDC_06
8
10 GND
IDC
IDC BOX Functions:
ISP Programing for ATmega128
SPI Pins : MOSI, MISO, SCK
TWI Pins : SDA, SCL
UART Pins : RXD0, TXD0
VCC
R1
±1%
1K5
USB
VCC
MH
V+ 1
DM 2
DP 3
V− 4
USB−B
GND
GND
R3
USB_D−
R2
68R
±1%
68R
USB_D+
D1
3V6
D2
3V6
GND
GND
VCC
C1
100µ
GND
VCC
C2
±5%
50V; NP0
100n
GND
12 MHz for USB/CAN/USART
16 MHz for USB/CAN/USART
XTL1
XTAL
XTL2
C3
±5%
16MHz
50V; NP0
22n
GND
C4
±5%
50V; NP0
22n
GND
AREF
C5
±5%
50V; NP0
100n
GND
Sheet: /
File: eda−crossover−altium.kicad_sch
Title:
Size: A
Date:
Rev:
KiCad E.D.A. 10.0.6
Id: #/1